

# SPECIFICATION FOR LCD MODULE MODULE NO: YB-TG240240C02D-N-A0

# Doc.Version:02

Customer Approval:

□ Accept

Reject

YEEBO	NAME	SIGNATURE	DATE
Prepare	Electronic Engineer	李嘉文	2022-09-27
Check	Mechanical Engineer		
Verify			
Approval			

#### DAPPROVAL FOR SPECIFICATIONS ONLY

■APPROVAL FOR SPECIFICATIONS AND SAMPLE

WIMRD005-02-D

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# **<u>1. Revision History</u>**

Sample Version	DOC. Version	DATE		CHANGED BY	
A0	00	2022-01-06	SPEC ONLY	First issue	LJW/HHK
A0	01	2022-02-11	SPEC ONLY	Modify LCM drawingP5	LJW/HHK
A0	02	2022-09-27	FULL SPEC	First Sample	LJW/MARK



# **<u>2. Table of Contents:</u>**

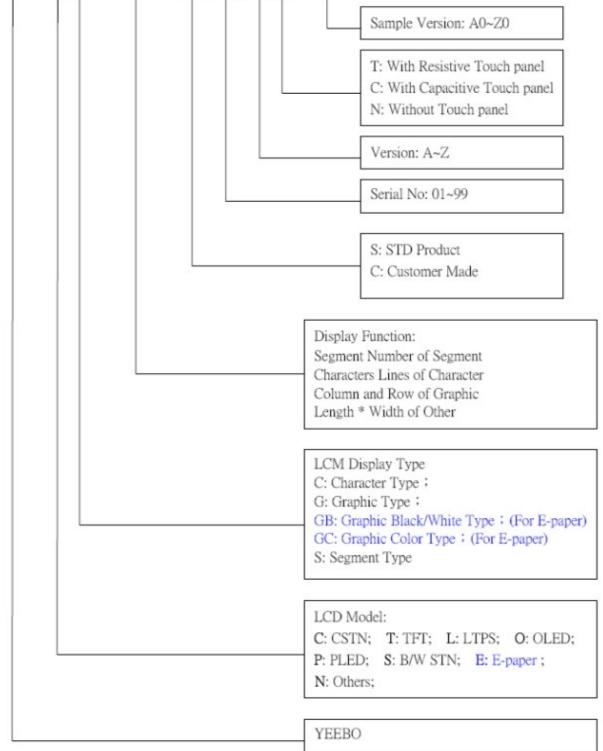
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**3.Module Numbering System:** 

(Example)

# <u>YB-TG240320S01D-T-A0</u>



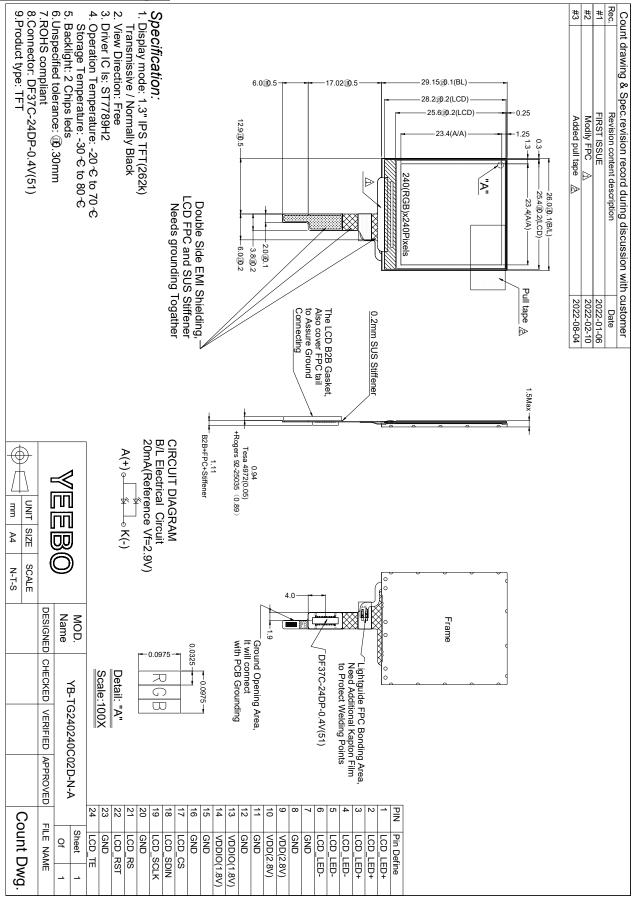


# 4. General Specification:

ITEM	CONTENTS		
Module Size	26.0 (W) * 29.15 (H) * 1.5 (T) mm		
Module Size(With FPC)	26.0 (W) * 52.17 (H) * 1.5(T) mm		
Display Size(Diagonal)	1.3 inch		
Display Format	240(RGB)*240 Pixels		
Active Area	23.4(W) * 23.4 (H) mm		
Pixel Pitch	0.0975 * 0.0975 mm		
LCD Type	TFT (262K) / Transmissive / Normally Black		
View Angle	Free		
Controller IC	ST7789H2		
Weight	2.2g		



# 5. LCM drawing:



Module P/N: YB-TG240240C02D-N-A0 Doc.Version:02



# <u>6. Electrical Characteristics</u> 6-1 Absolute Maximum Ratings

#### (Ta=25°C

VSS=0V)

Item	Symbol	Min.	Туре	Max.	Unit	Remark
Supply Voltage	VDD	-0.3	-	+4.6	Volt	Note1
Supply Voltage(Logic)	VDDIO	-0.3	-	+4.6	Volt	Note1
Operating Temperature	Topr	-20	-	+70	°C	-
Storage Temperature	Tstg	-30	-	+80	°C	-

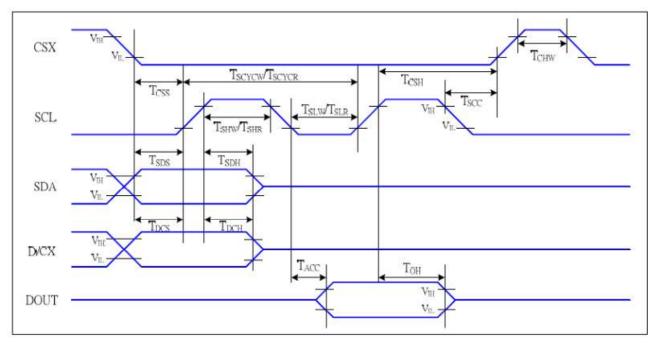
Note1: Absolute maximum rating is the limit value beyond which the IC maybe broken. They do not assure operations.

6-2 Operating	(Ta	(Ta=25°C)				
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Voltage VDI		- 2.4		2.8	3.3	Volt
Supply voltage for I/O	VDDIO	-	1.65	1.8	3.3	Volt
Input Voltage	V <sub>IH</sub>	-	0.7 VDDIO	-	VDDIO	V
	V <sub>IL</sub>	-	GND	-	0.3 VDDIO	V
Power Supply Current for LCM	Idd	VDD =2.8V	-	6	9	mA



#### **6-3 Timing Characteristics**

6-3.1 Display Serial Interface Timing Characteristics (4-line SPI system)



VDD/=1.65 to 3.3V,	VDD=2.4 to 3.3V,	AGND=DGND=0V,	Ta=-30 to 70 C
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Signal	Symbol	Parameter	MIN	MAX	Unit	Description
	T <sub>CSS</sub>	Chip select setup time (write)	15		ns	
	T <sub>CSH</sub>	Chip select hold time (write)	15		ns	
CSX	T <sub>CSS</sub>	Chip select setup time (read)	60		ns	
	T <sub>scc</sub>	Chip select hold time (read)	65		ns	
	T <sub>CHW</sub>	Chip select "H" pulse width	40		ns	
	T <sub>SCYCW</sub>	Serial clock cycle (Write)	16		ns	unite command 8 data
	T <sub>SHW</sub>	SCL "H" pulse width (Write)	7		ns	-write command & data
	T <sub>SLW</sub>	SCL "L" pulse width (Write)	7		ns	ram
SCL	TSCYCR	Serial clock cycle (Read)	150		ns	read as merced 9 data
	T <sub>SHR</sub>	SCL "H" pulse width (Read)	60		ns	-read command & data
	T <sub>SLR</sub>	SCL "L" pulse width (Read)	60		ns	ram
DIOX	T <sub>DCS</sub>	D/CX setup time	10		ns	
D/CX	TDCH	D/CX hold time	10		ns	
SDA	T <sub>SDS</sub>	Data setup time	7		ns	
(DIN)	T <sub>SDH</sub>	Data hold time	7		ns	
DOUT	T <sub>ACC</sub>	Access time	10	50	ns	For maximum CL=30pF
0001	Тон	Output disable time	15	50	ns	For minimum CL=8pF

Table 6 4-line serial Interface Characteristics

Note : The rising time and falling time (Tr, Tf) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals.



# 7. Optical Characteristics:

Itare	Item		Conditions	Spe	Specifications			Note
Item			Conditions	Min	Тур	Max	Unit	Note
Transmittance (With PL)		T(%)	-	3.65	4.3	-	-	-
Contrast Ratio		CR	Θ=0 Normal Viewing Angle	800	1200	-	_	(1)(2)
Response time		TR+TF	-	-	30	35	ms	(1)(3)
NTSC	2	%	-	45	50	-	-	-
	Hor	$\Theta x^+$		70	80	-		
Viewing	пог	θx-	CR≧10	70	80	-	dog	
angle		$\Theta$ y+		70	80	-	deg.	-
_	Ver	θy-		70	80	-		

Measuring Condition

1. Measuring surrounding: dark room

- 2. Ambient temperature: 25±2°C
- 3. 30 min. Warm-up time.

Color of CIE Coordinate:

Item		Symbol	Condition	Min.	Тур.	Max.
		x		0.538	0.588	0.638
	Red	у		0.275	0.325	0.375
	Green	x	$\theta = \phi = 0^{\circ}$ LED Backlight	0.275	0.325	0.375
Chromaticity Coordinates		у		0.487	0.537	0.587
(Transmissive)	Blue	x		0.104	0.154	0.204
		у		0.035	0.085	0.135
	XX 71 · 4	X		0.254	0.304	0.354
	White	у		0.277	0.327	0.377



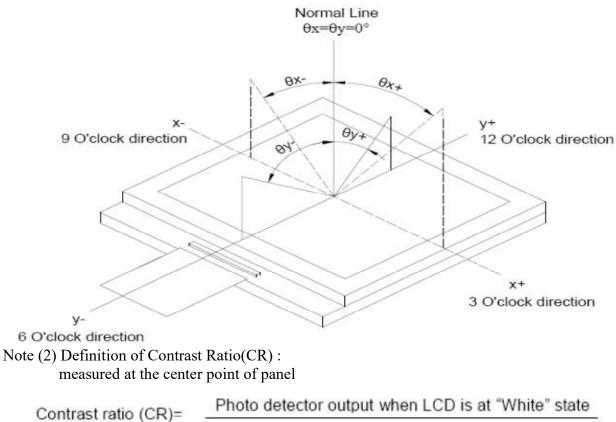
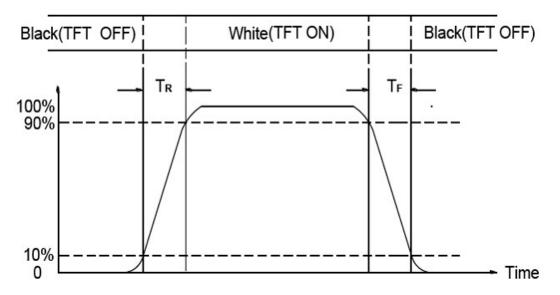


Photo detector output when LCD is at "Black

Note (3) Definition of Response Time : Sum of TR and TF



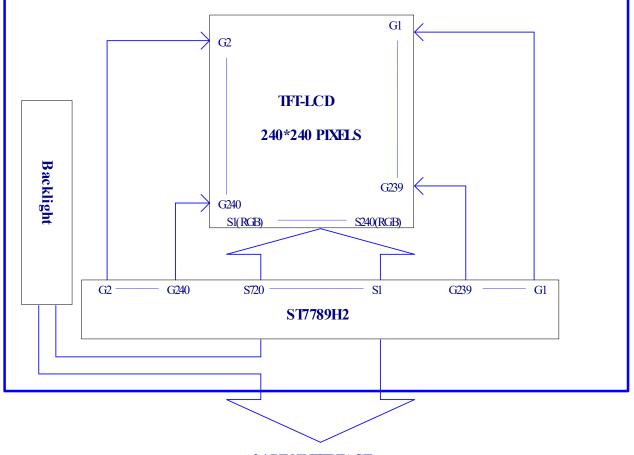


# 8. Interface Pin Assignment:

No.	Symbol	Function
1~3	LCD_LED+	LED power anode
4~6	LCD_LED-	LED power cathode
7~8	GND	Ground
9~10	VDD	Analog power supply(2.8V)
11~12	GND	Ground
13~14	VDDIO	Supply voltage for I/O(1.8V)
15~16	GND	Ground
17	LCD_CS	Chip select signal
18	LCD_SDIN	Serial data input/output
19	LCD_SCLK	Serial interface clock
20	GND	Ground
21	LCD_RS	Display data/command selection pin in 4-line serial interface
22	LCD_RST	Reset signal
23	GND	Ground
24	LCD_TE	Tearing effect signal is used to synchronize MCU to frame memory (No connection)



# 9. Block Diagram:



**24 PIN INTERFACE** 



# 10. Backlight:

- 1. Standard Lamp Styles (Edge Lighting Type): The LED chips are distributed over the edge light area of the illumination unit, which gives the less power consumption:
- 2. The Main Advantages of the LED Backlight are as following:
  - 2.1 The brightness of the backlight can simply be adjusted. By a resistor or a potentiometer.

3.	Data	About	LED	Back	light:
۰.		1 10 0 000			

(Ta=25℃)

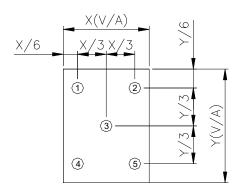
3. Dua Robat EED Backinght.					14 20 0		
PARAMETER	Sym.	Min.	Тур.	Max.	Unit	Test Condition	Note
Supply Current	Ι	-	20	-	mA		
Supply Voltage	V	2.8	3.1	3.4	V	If=20mA	
Luminous Intensity for LCM	IV	250	320	-	Cd/m2		2
Uniformity for LCM	-	70	-	-	%	If=20mA	3
Life Time	-	20000	_	_	Hr.		4
Color	White						

NOTE:

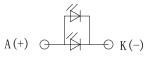
- 1. Backlight Only
- 2. Average Luminous Intensity of P1-P5
- 3. Uniformity = Min/Max \* 100%
- 4. LED life time defined as follows: The final brightness is at 50% of original brightness

Measured Method: (X\*Y: Light Area)

**Internal Circuit Diagram** 



(Effective spatial Distribution) Using aperture of 1°, distance 50cm. Circuit Diagram(LED 2 PCS)





# 11. Standard Specification for Reliability :

11-1. Standard Specifications for Reliability of LCD Module

No	Item	Description
01	High temperature operation	The sample should be allowed to stand at 70°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
02	Low temperature operation	The sample should be allowed to stand at -20°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
03	High temperature storage	The sample should be allowed to stand at 80°C for 240 hours under no-load condition, and then returning it to normal temperature condition, and allowing it stand for 2 hours.
04	Low temperature storage	The sample should be allowed to stand at -30°C for 240 hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 2 hours.
05	Moisture storage	The sample should be allowed to stand at 60°C,90%RH MAX for 240 hours under no-load condition, then taking it out and drying it at normal temperature for 2 hours.
06	Thermal shock storage	The sample should be allowed to stand the following 10 cycles : -30°C for 30 minutes $\rightarrow$ normal temperature for 5 minutes $\rightarrow$ +80°C for 30 minutes $\rightarrow$ normal temperature for 5 minutes, as one cycle.
07	Packing vibration	Frequency range : 10Hz ~ 55Hz Amplitude of vibration : 1.5mm X,Y,Z 2 hours for each direction. Sweep time: 12 min
08	Packing drop test	According to ISTA 1A 2001.
09	Electrical Static Discharge	Air: ±4KV 150pF/330Ω 5 times
	ple size for each test ite	Contact: $\pm 2KV \ 150 pF/330\Omega \ 5$ time

\*Sample size for each test item is 3~5pcs



11 - 2. Testing Conditions and Inspection Criteria

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in Table 12.1, Standard specifications for Reliability have been executed in order to ensure stability.

No	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

#### 11-3. MTBF

MTBF	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ( $25\pm5^{\circ}$ C), normal humidity ( $50\pm10\%$ RH), and in area not exposed to direct sun light.
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# **12. Specification of Quality Assurance:**

12-1. Purpose

This standard for Quality Assurance should affirm the quality of LCD module products to supply to purchaser by YEEBO CORPORATION (Supplier).

12-2. Standard for Quality Test

a. Inspection:

Before delivering, the supplier should take the following tests, and affirm the quality of product.

b. Electro-Optical Characteristics:

According to the individual specification to test the product.

- c. Test of Appearance Characteristics:
  - According to the individual specification to test the product.
- d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

e. Delivery Test:

Before delivering, the supplier should take the delivery test.

(i) Test method: According to **ISO2859-1.**General Inspection Level II take a single time.

(ii) The defects classify of AQL as following:

Major defect: AQL = 0.40

- Minor defect: AQL = 1.0
- Total defects: AQL = 1.0
- 12-3. Non- conforming Analysis & Deal With Manners

a. Non- conforming Analysis:

- (i) Purchaser should supply the detail data of non- conforming sample and the non-conforming.
- (ii) After accepting the detail data from purchaser, the analysis of non- conforming should be finished in two weeks.
- (iii) If supplier can not finish analysis on time, must announce purchaser before 3 days.
- b. Disposition of non- conforming:
  - (i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.
  - (ii) Both supplier and customer should analyze the reason and discuss the disposition of non- conforming when the reason of nonconforming is not sure.

12-4. Agreement items

Both sides should discuss together when the following problems happen.

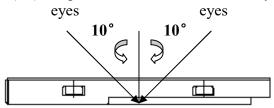
- a. There is any problem of standard of quality assurance, and both sides should think that must be modified.
- b. There is any argument item which does not record in the standard of quality assurance.
- c. Any other special problem.



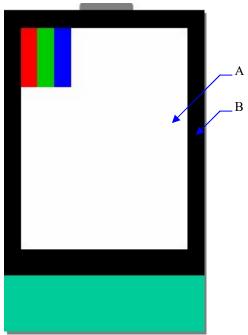
- 12-5. Standard of The Product Appearance Test
  - a. Manner of appearance test:

(i) The test must be under 20W  $\times$  2 or 40W fluorescent light, and the distance of view must be at 30±5cm.

- (ii) When test the model of transmissive product must add the reflective plate.
- (iii)The test direction is base on around 10° of vertical line.
- (iiii)Temperature: 25±5°C Humidity: 60±10%RH



(iv) Definition of area:



A. Area: Viewing area.

B. Area: Out of viewing area. (Outside viewing area)

b. Basic principle:

- (i) It will accord to the AQL when the standard can not be described.
- (ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
- (iii) Must add new item on time when it is necessary.

c. Standard of inspection: (Unit: mm)



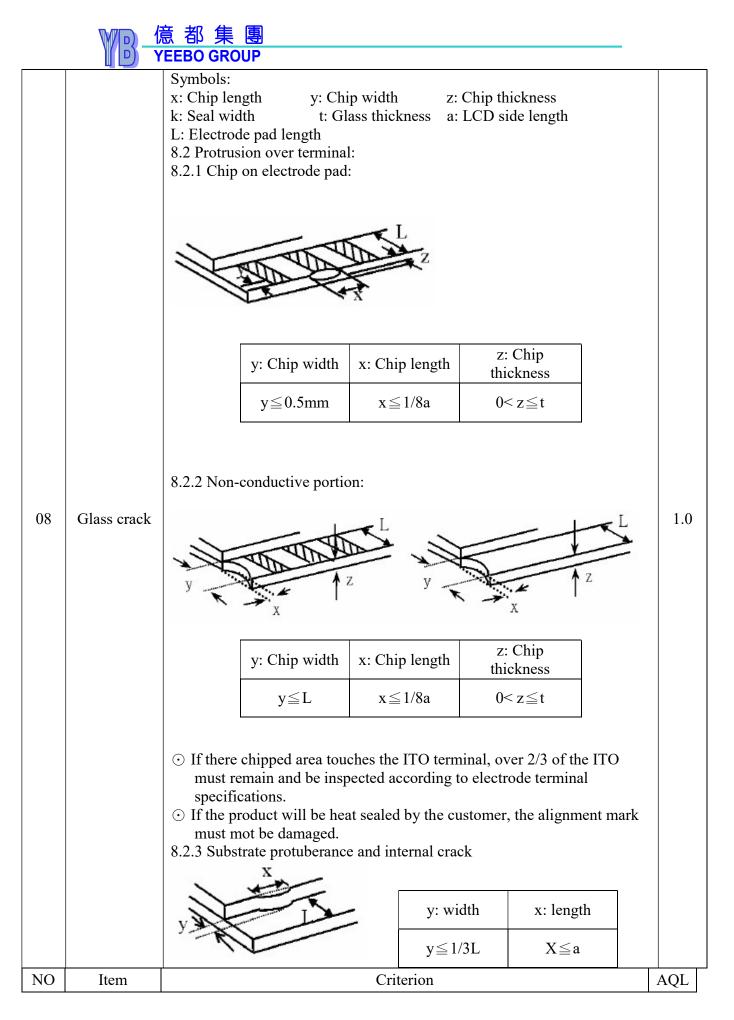
12-6. Inspection specification

	-	-		
		wing area	1	1 / 1
I latant o	111 AT 1/10	WING OFOO	con ho n	anlantad
	ul ul vic	מועם מועם	, can de n	Cenculua.

NO	Item	viewing area can be neg		iterion		AQL
01	Electrical Testing	<ol> <li>1.1 Missing vertical, horizontal segment, segment contrast defect.</li> <li>1.2 Missing character, dot or icon.</li> <li>1.3 Display malfunction.</li> <li>1.4 No function or no display.</li> <li>1.5 Current consumption exceeds product specifications.</li> <li>1.6 LCD viewing angle defect.</li> <li>1.7 Mixed product types.</li> <li>1.8 Flicker</li> </ol>			0.4	
02	Black or White spots or Bright spots or Color spots on LCD (Display only)	<ul><li>2.1 White and black or Five spots.</li><li>2.2 Densely spaced: No</li><li>2.3 Not visible through</li></ul>	o more than 5% ND fil	a three spots with ter		1.0
	LCD and Touch Panel black spots,		ely spaced:	Size(mm) $\Phi \leq 0.10$ $0.10 < \Phi \leq 0.20$ $0.20 < \Phi \leq 0.25$ $0.25 < \Phi \leq 0.30$ $0.30 < \Phi$ No more than two	Acceptable Q'ty Accept no dense 2 2 1 0 vo spots within 3mm.	1.0
03	white spots, contamination (non – display)	3.2 Line type: (As follo $\downarrow W$ $\downarrow L$ $\downarrow W$	Length( mm) $L \leq 3.0$ $L \leq 2.0$ 	ing) Width(mm) W≦0.02 0.02 <w≦0.05 0.05<w≦0.07 0.075<w< td=""><td>Acceptable Q'ty Accept no dense 5 2</td><td>1.0</td></w<></w≦0.07 </w≦0.05 	Acceptable Q'ty Accept no dense 5 2	1.0

NO	Item	Criterion	AQL
04	Polarizer bubbles		1.0

If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify directionSize $\Phi(mm)$ Acceptable $Qty$ Area $Qty$ $\Phi \leq 0.15$ Accept noV.A $\Phi \leq 0.15$ Accept noV.A $0.15 < \Phi \leq 0.3$ 2VA $0.30 < \Phi \leq 0.5$ 1VA $0.50 < \Phi \leq 1$ 2Out of $05$ ScratchesFollow NO.3 - 2 Line Type.0 $06$ MuraNot visible through 5% ND filter in 50% gray.1.0Symbols: x: Chip length x: Chip length t: Glass thickness k: Scal width t: Glass thickness t: Class thickness a: LCD side length T.1 General glass chip: 7.1.1 Chip on panel surface and crack between panels: $0.16 < 4.00 < -1.00 < 0.00 time07Chippedglass\overline{z}Chip hicknesst Z \leq 1/2t\overline{z}\overline{z}\overline{z}0.11Chip perfect Z \leq 1.00 t not over viewingZ \leq 1/2tNot exceed 1/3kX \leq 1/8aZ \leq 1/2t\overline{z}\overline{z}0.11Chippedglass\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}0.11\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}0.11\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}0.11\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}0.11\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}0.11\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}\overline{z}0.$		WB-	意都集團 YEEBO GROUP				
$07  Chipped glass  07 Chipped glass  07 Chipped \\ 07 \\ 07 \\ Chipped \\ 07 \\ 07 \\ Chipped \\ 07 \\ 07 \\ 07 \\ 07 \\ 07 \\ 07 \\ 07 \\ 0$							
07  Chipped glass  07 $Chipped glass  07 Chipped glass  07 Chipped glass  07 Chipped glass  07 Chipped glass  0 5 in d, must check in generative for the form of the $			judge using black spot specifications, not easy	Size Φ(mm)	-	Area	
$0.30 \le \Phi \le 0.5 \qquad 1 \qquad VA \\ \hline 0.50 \le \Phi \le 1 \qquad 2 \qquad Out of \\ VA \\ \hline 1 \le \Phi \qquad 0 \qquad - \\ \hline 05 \qquad Scratches \qquad Follow NO.3 - 2 Line Type. \\ \hline 06 \qquad Mura \qquad Not visible through 5% ND filter in 50% gray. \\ \hline 106 \qquad Symbols: \\ x: Chip length y: Chip width z: Chip thickness \\ k: Seal width t: Glass thickness a: LCD side length \\ L: Electrode pad length \\ 7.1 General glass chip: \\ 7.1.1 Chip on panel surface and crack between panels: \\ \hline \hline 2 \le Chip thickness y: Chip width x: Chip length \\ Z \le 1/2t \qquad Not over viewing x \le 1/8a \\ \hline 07 \qquad Chipped glass \\ \hline 07 \qquad Chipped \\ glass \qquad \hline \frac{z: Chip thickness y: Chip width x: Chip length}{Z \le 1/2t \qquad Not over viewing x \le 1/8a} \\ \hline 0.1.2 \ Chip thickness y: Chip width x: Chip length \\ \hline 1.1 Cher are 2 or more chips, x is the total length of each chip \\ \hline 7.1.2 \ Corner crack: \\ \hline \hline 1.2 \ Chip thickness y: Chip width x: Chip length \\ \hline Z \le 1/2t \qquad Not over viewing x \le 1/8a \\ \hline 0.1 \ fthere are 2 or more chips, x is the total length of each chip \\ \hline 7.1.2 \ Corner crack: \\ \hline \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore are 2 > 0 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 0$				Φ≦0.15	Accept no	V.A	
$0.30 \le \Phi \le 0.5 \qquad 1 \qquad VA \\ \hline 0.50 \le \Phi \le 1 \qquad 2 \qquad Out of \\ VA \\ \hline 1 \le \Phi \qquad 0 \qquad - \\ \hline 05 \qquad Scratches \qquad Follow NO.3 - 2 Line Type. \\ \hline 06 \qquad Mura \qquad Not visible through 5% ND filter in 50% gray. \\ \hline 106 \qquad Symbols: \\ x: Chip length y: Chip width z: Chip thickness \\ k: Seal width t: Glass thickness a: LCD side length \\ L: Electrode pad length \\ 7.1 General glass chip: \\ 7.1.1 Chip on panel surface and crack between panels: \\ \hline \hline 2 \le Chip thickness y: Chip width x: Chip length \\ Z \le 1/2t \qquad Not over viewing x \le 1/8a \\ \hline 07 \qquad Chipped glass \\ \hline 07 \qquad Chipped \\ glass \qquad \hline \frac{z: Chip thickness y: Chip width x: Chip length}{Z \le 1/2t \qquad Not over viewing x \le 1/8a} \\ \hline 0.1.2 \ Chip thickness y: Chip width x: Chip length \\ \hline 1.1 Cher are 2 or more chips, x is the total length of each chip \\ \hline 7.1.2 \ Corner crack: \\ \hline \hline 1.2 \ Chip thickness y: Chip width x: Chip length \\ \hline Z \le 1/2t \qquad Not over viewing x \le 1/8a \\ \hline 0.1 \ fthere are 2 or more chips, x is the total length of each chip \\ \hline 7.1.2 \ Corner crack: \\ \hline \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore are 2 > 0 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 1/2t < z \le 2t \qquad Not exceed 1/3k \qquad x \le 1/8a \\ \hline 0.1 \ Hore area \\ \hline 0$				$0.15 < \Phi \le 0.3$	2	VA	
$07  Chipped glass \qquad $							
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05ScratchesFollow NO.3 - 2 Line Type.06MuraNot visible through 5% ND filter in 50% gray.1.0Symbols: x: Chip length x: Chip length T.1 General glass chip: 7.1.1 Chip on panel surface and crack between panels:z: Chip thickness k: Seal width T.1 General glass chip: 						VA	
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$07  \begin{array}{ c c c c c c c } Chipped \\ glass \\ \hline \hline z: Chip hickness & \underline{y}: Chip width & \underline{x}: Chip hickness & \underline{x}: LCD side length \\ \hline L: Electrode pad length \\ \hline 7.1 General glass chip: \\ \hline 7.1.1 Chip on panel surface and crack between panels: \\ \hline \hline \hline \hline \hline & & & & & & & & & & & & & &$	06	Mura		D filter in 50% gray	/ <b>.</b>		1.0
$Z \le 1/2t$ Not over viewing area $x \le 1/8a$ $\odot$ $1/2t < z \le 2t$ Not exceed $1/3k$ $x \le 1/8a$ unit:	07		k: Seal width t: C L: Electrode pad length 7.1 General glass chip: 7.1.1 Chip on panel surface $\overline{z: Chip thickness}  \underline{y: C}$ $\overline{Z \leq 1/2t}  Nc$ $1/2t \leq z \leq 2t  Nc$ $\odot$ If there are 2 or more c	Blass thickness a: The and crack betwee area this width the over viewing area tot exceed 1/3k	LCD side length n panels: x: Chip length $x \le 1/8a$ $x \le 1/8a$	Unit: mm	1.0
			$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	ot over viewing area fot exceed 1/3k	$x \le 1/8a$ $x \le 1/8a$	Unit:	
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Module P/N: YB-TG240240C02D-N-A0 Doc.Version:02

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09	Cracked glass	The LCD with extensive crack is not acceptable.	1.0
10	Backlight elements	<ul> <li>10.1 Illumination source flickers when lit.</li> <li>10.2 Spots or scratches that appear when lit must be judged. Using LCD spot, lines and contamination standards.</li> <li>10.3 Backlight doesn't light or color is wrong.</li> </ul>	1.0 1.0 0.4
11	Bezel	Bezel must comply with product specifications.	1.0
12	РСВ、СОВ	<ul> <li>12.1 COB seal may not have pinholes larger than 0.2mm or contamination.</li> <li>12.2 COB seal surface may not have pinholes through to the IC.</li> <li>12.3 The height of the COB should not exceed the height indicated in the assembly diagram.</li> <li>12.4 There may not be more than 2mm of sealant outside the seal area on PCB. And there should be no more than three places.</li> <li>12.5 Parts on PCB must be the same as on the production characteristic chart, There should be no wrong parts, missing parts or excess parts.</li> <li>12.6 The jumper on the PCB should conform to the product characteristic chart.</li> <li>12.7 PCBA cosmetic control base on latest IPC standard, IPC-A-610, acceptalbe limit of grade 2.</li> </ul>	$ \begin{array}{c} 1.0\\ 1.0\\ 1.0\\ 0.4\\ 0.4\\ 1.0\\ \end{array} $
13	FPC	13.1 FPC terminal damage $\leq 1/2$ FPC terminal width and can not affect the function, we judge accept. 13.2 FPC alignment hole damage $\leq 1/2$ alignment area and can not affect the function, we judge accept.	1.0 1.0
14	Soldering	<ul><li>14.1 No cold solder joints, missing solder connections, oxidation or icicle.</li><li>14.2 No short circuits in components on PCB or FPC.</li></ul>	1.0 0.4



NO Item		AQL
NO Item	Criterion         Symbols:       x: Chip length       y: Chip width       z: Chip thickness         k: Seal width       t: Touch Panel Total thickness       a: LCD side         length       L: Electrode pad length         15.1 General glass chip:         15.1.1 Chip on panel surface and crack between panels:         Image: Chip width         x: Chip thickness         y: Chip width         x: Chip thickness         y: Chip width         x: Chip length         x: Chip thickness         y: Chip width         x: Chip length         x: Chip thickness         y: Chip width         x: Chip length	AQL
Touch Pa 15 Chippe glass	anel ed $Z \leq t$ $\leq 1/2$ k and not over viewing area $x \leq 1/8a$ $\odot$ Unit: mm $\odot$ If there are 2 or more chips, x is the total length of each chip	1.0
	15.1.2 Corner crack:Image: state of the state of	

NO	Item	Criterion	AQL			
16	Touch Panel(Fish eye、dent		1.0			
	Module $P/N$ · VB-TG240240C02D-N-A0					

₩B			
	and bubble on film)	SIZE(mm)Acceptable Q'ty $\Phi \leq 0.2$ Accept no dense $0.2 < D \leq 0.4$ 5 $0.4 < D \leq 0.5$ 2 $0.5 < D$ 0	
17	Touch Panel Newton ring	Newton ring dimension $\leq 1/2$ touch panel area and not affect font and line distortion( $\leq 2.5\%$ ), it is acceptable.	1.0
18	Touch Panel Linearity	Less than 2.5% is acceptable.	1.0
19	LCD Ripple	Touch the touch panel, can not see the LCD ripple. Pen: R 1.0mm silicon rubber. Operation Force: 80g	1.0
20	General appearance	<ul> <li>20.1 Pin type must match type in specification sheet.</li> <li>20.2 LCD pin loose or missing pins.</li> <li>20.3 Product packaging must the same as specified on packaging specification sheet.</li> <li>20.4 Product dimension and structure must conform to product specification sheet.</li> </ul>	0.4 0.4 0.4 0.4



### **13. Handling Precaution:**

13-1 Handling of LCM

- Don't give external shock.
- Don't apply excessive force on the surface.
- Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.
- Don't operate it above the absolute maximum rating.
- Don't disassemble the LCM.
- The operators should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- The modules should be kept in antistatic bags or other containers resistant to static for storage.
- The module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

#### 13-2 Storage

- Store in an ambient temperature of 25±10°C, and in a relative humidity of 50±10%RH. Don't expose to sunlight or fluorescent light.
- Storage in a clean environment, free from dust, active gas, and solvent.
- Store in anti-static electricity container.
- Store without any physical load.

#### 13-3 Soldering

- Use only soldering irons with proper grounding and no leakage.
- Iron: No higher than  $310\pm10^{\circ}$ C and less than 3 sec during Hand soldering.
- Rewiring: no more than 2 times.



## 14. Warranty

This product has been manufactured to specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we will not take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

1. We cannot accept responsibility for any defect arise after additional process of the product (including disassembly) and reassembly), after product delivery.

2. We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.

3. We cannot accept responsibility for any defect, which may arise due to the application of static

electricity after the product has passed your company's acceptance inspection procedures.

4. We can not accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product within one year from YEEBO shipment.

5. For Heatseal Product which required to heatseal by customer side, parts must be used within three months after delivery from factory.

6. For TAB Product which required to solder by customer side, parts must be used within three months after delivery from factory.

7. The liability of YB is limited to repair or replacement on the terms set forth below. YB will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between YB and the customer, YB will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with YB GENERAL LCD INSPECTION STANDARD.

#### 15. Guarantee:

Our products meet requirements of the environment.

YEEBO ROHS requirement is based on European Union Directive 2011/65/EU(ROHS) Requirements and Update.