

Customer Approval:

# SPECIFICATION FOR CTP MODULE MODULE NO: YB-TG1280800C26A-C-A

Doc.Version:10

□ Accept	C Reject

YEEBO	NAME	SIGNATURE	DATE
Prepare	Electronic Engineer	与龟献	2021/12/17
Check	Mechanical Engineer	资本后	2021/12/17
Verify			
Approval		子、西方	2021/12/17

□ APPROVAL FOR SPECIFICATIONS ONLY

■ APPROVAL FOR SPECIFICATIONS AN SAMPLE

WIMRD005-02-C



# **1. Revision History**

Sample Version									DATEDESCRIPTIONCHANG BY		
A0	00	2021-05-08	SPEC ONLY	First issue	ZZZ						
A0	01	2021-05-18	SPEC ONLY	Modify TFT	ZZZ						
A0	02	2021-05-26	SPEC ONLY	Update the backlight life	ZZZ						
A0	03	2021-05-28	SPEC ONLY	Update TFT working temperature	ZZZ						
A0	04	2021-06-17	SPEC ONLY	Update FPC shape	ZZZ						
A0	05	2021-06-29	SPEC ONLY	Update TFT	ZZZ						
A0	06	2021-7-1	SPEC ONLY	Update Spec	ZZZ						
A0	07	2021-7-19	SPEC ONLY	Update TFT	ZZZ						
A0	08	2021-8-17	Full SPEC	First sample	ZZZ						
A0	09	2021-9-22	Full SPEC	Update Spec	ZZZ						
A0	10	2021-12-17	Full SPEC	Update the backlight life	ZWL						

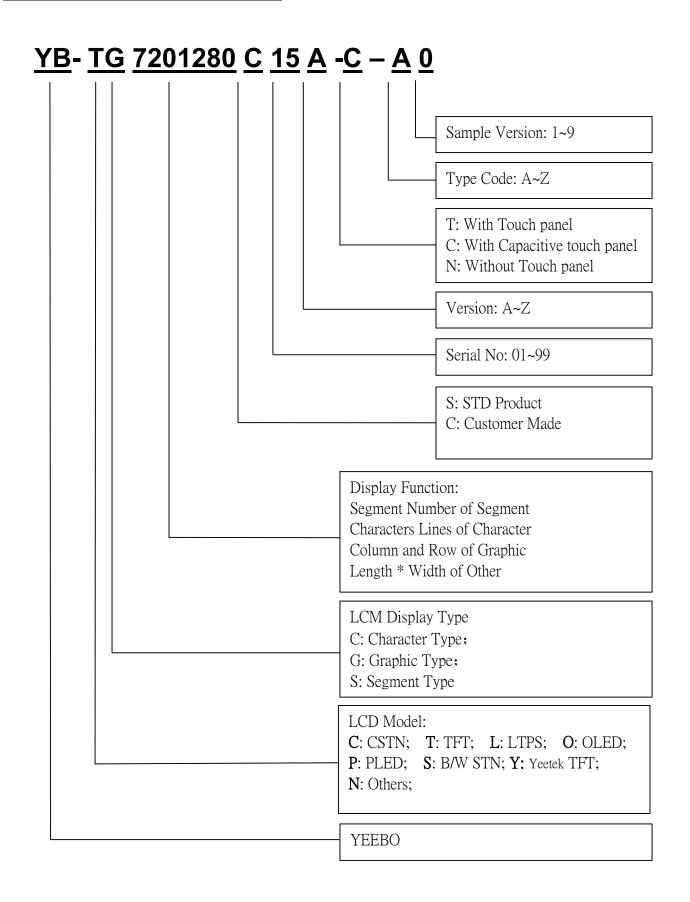


# **2. Table of Contents:**

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# 3. Module Numbering System:





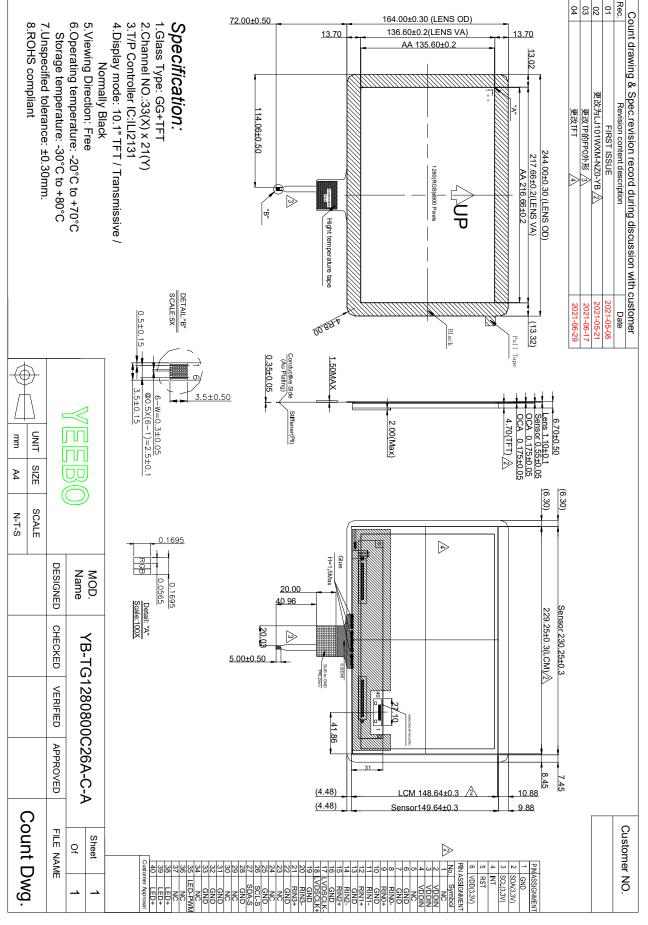
# 4. General Specification:

ITEM	CONTENTS
Module Size	244(W) * 164(H) *6.70 (T) mm
Module Size(With FPC)	244(W) * 164(H) *6.70(T) mm(Without FPC)
Display Size(Diagonal)	10.1inch
Display Format	1280(RGB) * 800 Pixels
Pixel Pitch	0.0565 (H) <b>*</b> RGB* 0.1695 mm
LCD Type	TFT(16.7M) / Transmissive / Normal Black
Active Area	216.66(W) * 135.6(H) mm
View Angle	Free
TFT Interface	LVDS
CTP IC	ILI2131
CTP Interface	I2C
Weight(g)	$\approx 300$
Fireware	ILI2131-9547-20210816.hex
Test Configuration	9547_20210816-1.ini

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# 5. Drawing:



Module P/N: YB-TG1280800C26A-C-A Doc.Version:10



# <u>6. Electrical Characteristics</u> 6-1 <u>CTP Electrical Characteristics</u> 6-1-1 TP Absolute Maximum Ratings

(Ta=25°C VSS=0V)

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

Parameter	Symbol	Min	Max	Unit
USB 5V input power supply voltage	VDD5V	-0.3	6.0	v
VDD3A to GND	VDD3A	-0.3	3.6	V
VDDD to GND	Vooso	-0.3	3.6	V
VDBID to GND	Vppio	-0.3	3.6	V
V <sub>DD16</sub> to GND	VDD16	-0.3	1.65	V
V <sub>GH</sub> to GND	V <sub>GH</sub>	-0.3	32	V
V <sub>TX</sub> to GND	V <sub>TX</sub>	-0.3	32	V
ESD Susceptibility HBM (Human Body Mode)(Note 1)	нвм	2	4000	V
ESD Susceptibility MM (Machine Mode)	MM		400	V

#### Table 5-1: Absolute Maximum Ratings

Note 1: Devices are ESD sensitive. Handling precaution is recommended.



# 6-1-2 TP Operating Conditions

(Ta=25°C)

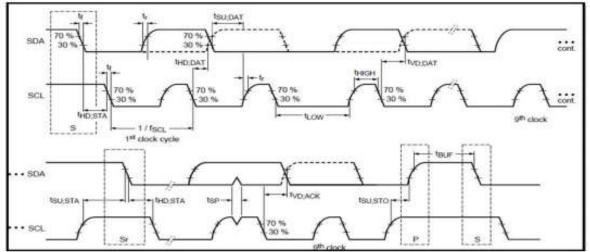
## **Recommended Operating Conditions**

Parameter	Symbol	Min	Max	Unit
USB 5V input power supply voltage	VDDSV	4.4	5.5	V
VDD3A to GND	VDDBA	3.0	3.6	V
VDD3D to GND	VDD3D	3.0	3.6	V
VDDIO tO GND	Vpoio	1.8	3.6	V
V <sub>GH</sub> to GND	V <sub>GH</sub>	-0.3	32	V
V <sub>TX</sub> to GND	V <sub>TX</sub>	-0.3	32	V
Operating Ambient Temperature Range	T <sub>A</sub>	-40	105	°C
Operating Junction Temperature Range	TJ	-40	125	°C
Storage Ambient Temperature Range	Tst	-40	150	°C

Note: The device is not guaranteed to function outside its operating conditions.

# 6-1-3 TP Timing Characteristics

TP I<sup>2</sup>C interface





Deremeter	Oursehart	Standard	mode	Fast-mod			
Parameter	Symbol	Min	Max	Min	Max	Unit	
SCL clock frequency	f <sub>SCL</sub>	0	100	0	400	kHz	
Hold time START condition	t <sub>HD;STA</sub>	4.0	1270	0.6		us	
LOW period of the SCL clock	t <sub>Low</sub>	4.7	S=	1.3	-	us	
HIGH period of the SCL clock	t <sub>High</sub>	4.0	2000	0.6	-	us	
Set-up time for a repeated START condition	t <sub>su:sta</sub>	4.7	3 <b>4</b> 1	0.6	*	us	
Data hold time	t <sub>HD;DAT</sub>	300	29	300	-	ns	
Data set-up time	t <sub>SU;DAT</sub>	250	-	100	-	ns	
Rise time of both SDA and SCL signals (30% to 70%)	tr	-	1000	20	300	ns	
Fall time of both SDA and SCL signals (70% to 30%)	tr		300	20	300	ns	
Set-up time for STOP condition	t <sub>su;sто</sub>	4.0	29-1	0.6	-	us	
Bus free time between a STOP and START condition	t <sub>BUF</sub>	4.7	).=)	1.3		us	
Capacitive load for each bus line	Cb	-	400	-	400	pF	
Noise margin at the LOW level for each connected device	V <sub>nL</sub>	0.1V <sub>DD</sub>	)=1	0.1V <sub>DD</sub>	~	v	
Noise margin at the HIGH level for each connected device	V <sub>nH</sub>	0.2V <sub>DD</sub>	0 80 <del>7</del> 0	0.2V <sub>DD</sub>		v	

## I2C AC Characteristics



# 6-2-1 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

Param	Parameter		Min.	Max.	Unit	Remarks
Power Suppl y	LCD Modu le	VDD	VSS-0.3	3.6	v	Ta = 25 ℃ Note 1&2
Operating Te	mperature	T <sub>OP</sub>	-20	+70	°C	
Storage Ten	nperature	T <sub>ST</sub>	-30	+80	°C	1

< Table 2. Environment Absolute Maximum Ratings >

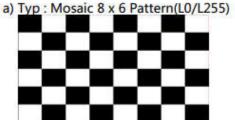


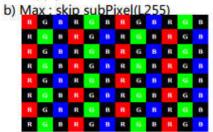
## 6-3.0 ELECTRICAL SPECIFICATIONS 3.1 TFT LCD Module

< Table 3. LCD Module Electrical specifications > [Ta = 25 ± 2 °C]

Parameter		Cumbel		Values			Neter
		Symbol	Min.	Тур.	Max.	Unit	Notes
DC	- L. M. Harra	VDD	3.0	3.3	3.6	V	1
Power Supply Voltage		VRP			300	mV	Ripple
Power Sup	ply Current	IDD	870	127	343	mA	
Power Co	onsumption	PLCD	520	0.42	1.13	W	Note 1
Rush	current	IRUSH		-	3.0	A	
	Input	VIH	2.7		3.3	V	
CMOS	Voltage	VIL	0		0.5	v	
Interface	Output	VOH	2.7		3.3	V	
	Voltage	VOL	0		0.5	V	

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM. The current draw and power consumption specified is for VDD=3.3V, Frame rate fv=60Hz and Clock frequency = 72.4MHz. Test Pattern of power supply current

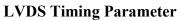


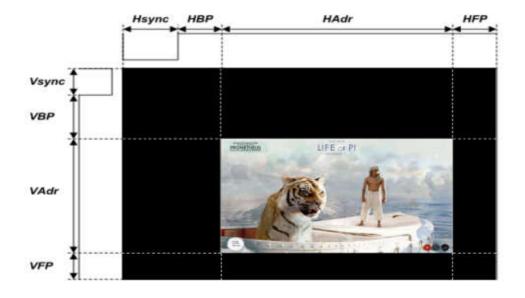




# 6-4 Interface timing Parameter

D			100			
Parameter	Symbol	Min. Typ.		Max.	Unit	
DCLK Frequency	Fdclk	66.3	72.4	78.9	MHz	
Horizontal display area Thd				pixel		
HSYNC period time	Th	1380	1440	1500	pixel	
HSYNC blanking	thbp+ thfp	100	160	220	pixel	
Vertical display area	Tvd	800			н	
Frequency	fV	55	60	65	Hz	
VSYNC period time	Tv	824	838	872	Н	
VSYNC blanking	Tvbp+ Tvfp	24	38	72	н	



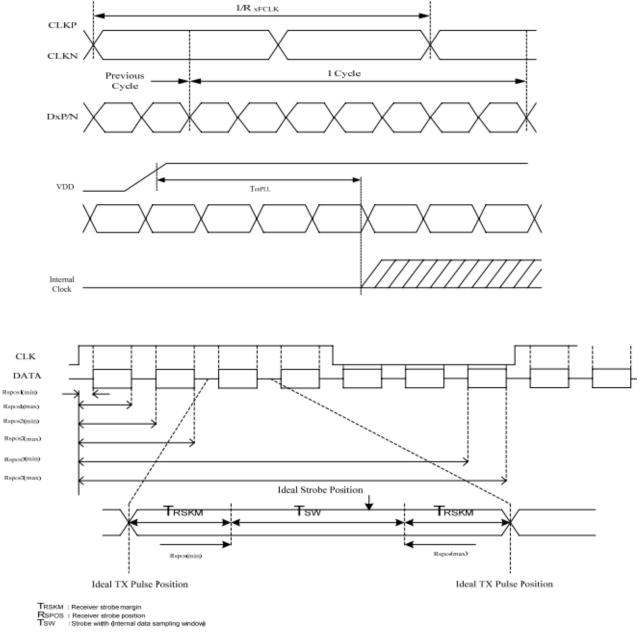




Item	Symbol	Min	Тур	Max	Unit	Remark
Clock frequency	RxFCLK	30	60	74.5	MHz	Refer to input tim ing table for each display resolution
Input data skew margin	TRSKM	500	175	ß	ps	VID  = 200mV RxVCM = 1.2V RxFCLK = 81M Hz
Clock high time	TLVCH	1000	4/(7* RxFCLK)	5	ns	
Clock low time	TLVCL	1843	3/(7* RxFCLK)	12	ns	0.

# LVDS AC Timing Specification

# 6-5 Interface timing Parameter

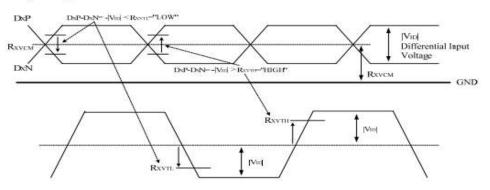




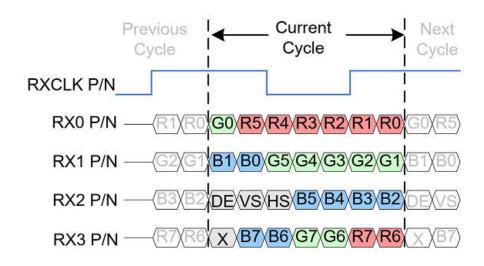
Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Differential input high t hreshold voltage	RXVTH	+ <mark>0.1</mark>	0.2	0.3	v	DVI/ON 1 OV
Differential input low th reshold voltage	RXVTL	-0.3	- <b>0</b> .2	-0.1	v	RXVCM=1.2V
Input voltage range (singled-end)	RXVIN	0.7	853	1.7	v	
Differential input comm on mode voltage	RXVCM	1	1.2	1.4	V	VID =0.2
Differential input imped ance	ZID	80	100	125	ohm	
Differential input voltag e	VID	0.2		0.6	v	
Differential input leaka ge current	ILCLVDS	-10	-	+10	uA	
LVDS Digital Operating Current	IVDDMIP I	12 .	15	20	mA	FDCLK=80MH z,VDD=3.3V, Input pattern: 55h->Aah->55h- >Aah
LVDS Digital Stand-by Current	ISTMIPI	-	-	250	uA	Clock & all Fun ctions are stoppe d

### LVDS DC Timing Specification

Single-end Signals

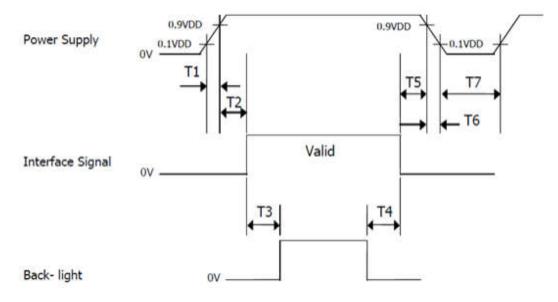


# Lane VESA Mode Color Bit Map





#### 6-6 Power Sequence



## Sequence Table

<b>n</b>		Values				
Parameter	Min	Тур	Max	Units		
T1	0	ō	10	ms		
T2	0	-	50	ms		
T3	200	́ е	8	ms		
T4	500	E.	Ei .	ms		
T5	0	×	50	ms		
T6	0	ē	10	ms		
<b>T</b> 7	500	-	-	ms		



# 7. Optical Characteristics:

### 7.1 Overview

The test of optical specifications shall be measured in a dark room (ambient luminance  $\leq 1$  lux and temperature = 25±2°C) with the equipment of Luminance meter system (Gonio meter system and TOPCON BM-5) and test unit shall be located at an approximate dista nce 50cm from the LCD surface at a viewing angle of  $\theta$  and  $\Phi$  equal to 0°. We refer to  $\theta \emptyset = 0$  (= $\theta 3$ ) as the 3 o' clock direction (the "right"),  $\theta \emptyset = 90$  (=  $\theta 12$ ) as the 12 O' clock direction ("upward"),  $\theta \emptyset = 180$  (=  $\theta 9$ ) as the 9 O' clock direction ("left") and  $\theta \emptyset = 27$  0(=  $\theta 6$ ) as the 6 O' clock direction ("bottom"). While scanning  $\theta$  and/or  $\emptyset$ , the center of the measuring spot on the Display surface shall stay fixed.

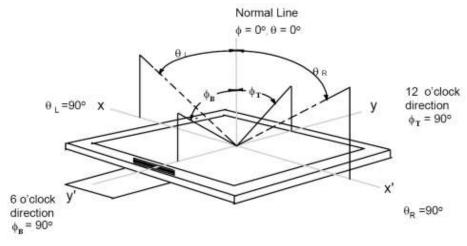
Item	Symbol	Condition	Min	Тур.	Max	Unit	Note
	θL		70	80		deg	
	θ <sub>R</sub>		70	80			NING 1
Viewing Angle	Ψτ	Cr≥10	70	80			Note 1
	ΨΒ		70	80		2	
Contrast Ratio	Cr	θ=0°		1000		(**)	Note 2
Response Time	Tr+Tf	FF=0°	1444	30	35	ms	Note 3
	Rx		0.588	0.618	0.648	-	
	Ry	]	0.299	0.329	0.359		
	Gx	]	0.253	0.283	0.313		
Color Coordinate of CIE1	Gy	θ=0°	0.512	0.542	0.572		Note 4
931	Bx	0=0	0.109	0.139	0.169		<u>@C光</u>
	By		0.134	0.164	0.194		
	Wx		0.269	0.299	0.329		
	Wy		0.309	0.339	0.369		2
NTSC Ratio	NTSC	CIE1931	<b>4</b> 5	50		%	Note 5
Polarization Direction of Front Polarizer	PdF			0		deg	Absorption
Polarization Direction of Rear Polarizer	PdR			90		deg	Note 7

#### 7.2 Optical Specifications



#### Note 1: The definition of Viewing Angle

Refer to the graph below marked by  $\theta$  and  $\phi$ .



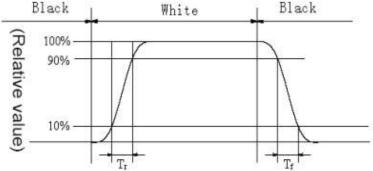
#### Note2:ThedefinitionofContrastRatio

Contrast Ratio(CR)= Luminance When LCD is at "White" state Luminance When LCD is at "Black" state

(Contrast Ratio is measured in optimum common electrode voltage)

Note3: DefinitionofResponse time. (Test LCD using RD80S or similar equipments):

The output sign also photo detector are measured when the input sign also are changed from "black" to "white" (Voltage falling time) and from "white" to "black" (Voltage rising time), respectively .Th e response time is defined as the time interval between the 10% and 90% of amplitudes . Refer to figu res below.



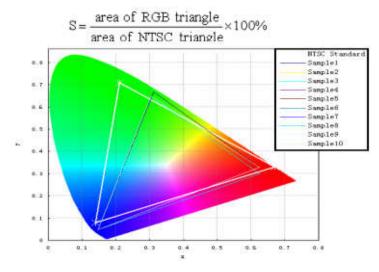
#### Note 4: Color Coordinates of CIE 1931

The test condition is at ILED=20mA and measured on the surface of LCD module at 25°C. Measurement equipment:CS2000 or similar equipments

The Color Coordinate (CIE 1931) is the measurement of the center of the display shown in below figure.

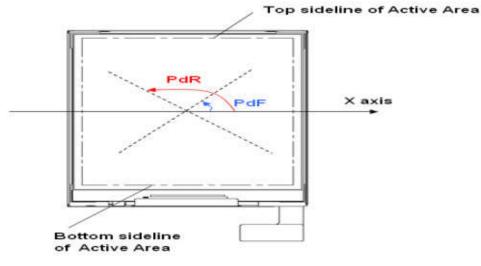


#### Note 5: Definition of Color of CIE Coordinate and NTSC Ratio.



#### Note 6: Polarization Direction Definition

- Viewing direction is normal user viewing direction which is vertical to the display surface
- The polarizer which is closer to viewer is defined as Front Polarizer
- •The polarizer which is on the rear side of viewer is defined as Rear Polarizer
- •The X axis is defined as parallel line to top & bottom sidelines of the Active Area
- •PdF which is marked in blue arrow is polarization degree of Front polarizer
- PdB which is marked in red arrow is polarization degree of Back polarizer
- The polarization degree parameter must be indicated in range of 0deg to 180deg according to abov e definition



#### Module P/N: YB-TG1280800C26A-C-A Doc.Version:10



# 8-1 TP FPC Interface

No.	Symbol	I/O	Function
1	GND	Р	Ground
2	SDA 3.3V	I/O	I <sup>2</sup> C Serial Data
3	SCL 3.3V	I/O	I <sup>2</sup> C Serial Clock
4	INT	0	Indicate coordinate data ready
5	RST	Ι	Active low external reset
6	VDD 3.3V	Р	Power Voltage for digital circuit

### **8.2 INPUT TERMINAL PIN ASSIGNMENT**

This LCD employs one interface connections, a 40 pin connector is used for the LCD module electronics interface.

#### 2.1 Pin assignment for LCD module

Connector: MSAK24025P40G (STM) or equivalent

< Table. Pin Assignment for LCD Module Connector >

Pin No.	Symbol	Description	I/O
1	NC	Non Connection	
2	VDDIN		Р
3	VDDIN	Power supply VDDIN=3.3V (Typ.)	P
4	VDDIN		P
5	NC	Non Connection	
6	GND	GROUND	P
7	GND	GROUND	P
8	RIN0-	LVDS Negative data signal (-)	I
9	RIN0+	LVDS Positive data signal (+)	I
10	GND	GROUND	P
11	RIN1-	LVDS Negative data signal (-)	I
12	RIN1+	LVDS Positive data signal (+)	I
13	GND	GROUND	P
14	RIN2-	LVDS Negative data signal (-)	I
15	RIN2+	LVDS Positive data signal (+)	I
16	GND	GROUND	P
17	LVDS_CLK-	LVDS Negative CLK signal (-)	I
18	LVDS_CLK+	LVDS Positive CLK signal (+)	I
19	GND	GROUND	P
20	RIN3-	LVDS Negative data signal (-)	I



Pin No.	Symbol	Description	I/O
21	RIN3+	LVDS Positive data signal (+)	1
22	GND	GROUND	P
23	NC	Non Connection	
24	NC	Non Connection	5
25	GND	GROUND	P
26	SCL_S	Reserved for LCD manufacturer' s use , not connection	
27	SDA_S	Reserved for LCD manufacturer' s use , not connection	
28	GND	GROUND	P
29	NC	Non Connection	
30	NC	Non Connection	5
31	GND		
32	GND	GROUND	P
33	GND		
34	NC	Non Connection	1
35	LED_PWM	LED PWM signal pin	I
38	NC	Non Connection	
37	NC	Non Connection	5
38	LED+		P
39	LED+	LED Anode	P
40	LED+	1	P

Note: I/O type: P=Power pin, I=Input pin, O=Output pin

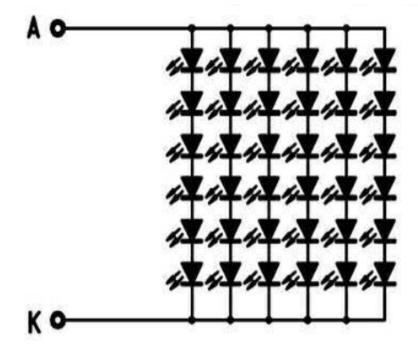


## 9. Backlight: ELECTRICAL SPECIFICATIONS Back-light Unit

Values Symbol Unit Notes Parameter Min. Тур. Max. LED voltage input range 9 VLED 12 21 V Value for Back light LED current input range 120 220 280 ILED mA -Value for Back light Luminous Intensity IV 400 Cd/m<sup>2</sup> 560 720 -LED Life Time 30000 Hr ---Backlight o En logic hig 1.2 VENH -18 h voltage **EN Control** n V level En logic lo Backlight of VENL --0.4 w voltage f **PWM High VPWH** 1.2 -18 -PWM Contr Level V ol level **PWM Low** -VPWL 0.4 --Level **PWM duty** \_ % 1 --**PWM** Ratio Control **PWM FPWM** 5 100 kHZ --Frequency

< Table 4. LED Driving guideline specifications > [Ta=25±2 °C]

### CIRCUIT DIAGRAM (LED 6 串 X 6 并=36dies)





# **10. Standard Specification for Reliability:**

	10–1. Standard Specifica	tions for Reliability of (LCD+CTP) Module
No	Item	Description
01	High temperature operation	The sample should be allowed to stand at $70^{\circ}$ C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
02	Low temperature operation	The sample should be allowed to stand at $-20^{\circ}$ C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
03	High temperature storage	The sample should be allowed to stand at $80^{\circ}$ C for 120 hours under no-load condition, and then returning it to normal temperature condition, and allowing it stand for 2 hours.
04	Low temperature storage	The sample should be allowed to stand at $-30^{\circ}$ C for 120 hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 2 hours.
05	Moisture storage	The sample should be allowed to stand at $40^{\circ}$ C,90%RH MAX for 120 hours under no-load condition, then taking it out and drying it at normal temperature for 2 hours.
06	Thermal shock storage	The sample should be allowed to stand the following 10 cycles : -0°C for 30 minutes $\rightarrow$ normal temperature for 5 minutes $\rightarrow$ +50°C for 30 minutes $\rightarrow$ normal temperature for 5 minutes, as one cycle.
07	Packing vibration	Frequency range : 10Hz ~ 55Hz Amplitude of vibration : 1.5mm Sweep time: 12 min X,Y,Z 2 hours for each direction.
08	Packing drop test	According to ISTA 1A 2001.

\*Sample size for each test item is 3~5pcs

10 - 2. Testing Conditions and Inspection Criteria



For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in Table 12.2, Standard specifications for Reliability have been executed in order to ensure stability.

No	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

#### 10-3. MTBF

MTBF	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 20,000 hours under ordinary operating and storage conditions room temperature ( $25\pm5^{\circ}$ C), normal humidity ( $50\pm10\%$ RH), and in area not exposed to direct sun light.
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# **<u>11. Specification of Quality Assurance:</u>**

#### 11-1. Pupose

This standard for Quality Assurance should affirm the quality of LCD module products to supply to purchaser by YEEBO CORPORATION (Supplier).

- 11-2. Standard for Quality Test
  - a. Inspection:

Before delivering, the supplier should take the following tests, and affirm the quality of

### product.

b. Electro-Optical Characteristics:

According to the individual specification to test the product.

- c. Test of Appearance Characteristics:
  - According to the individual specification to test the product.
- d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

e. Delivery Test:

Before delivering, the supplier should take the delivery test.

(i) Test method: According to MIL-STD105E.General Inspection Level II take a single time.

(ii) The defects classify of AQL as following:

Major defect: AQL = 0.65

Minor defect: AQL = 2.5

- Total defects: AQL = 2.5
- 11-3. Non- conforming Analysis & Deal With Manners
  - a. Non- conforming Analysis:
    - (i) Purchaser should supply the detail data of non- conforming sample and the non-conforming.
    - (ii) After accepting the detail data from purchaser, the analysis of non- conforming should be finished in two weeks.
    - (iii) If supplier can not finish analysis on time, must announce purchaser before 3 days.
  - b. Disposition of non- conforming:
    - (i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.
    - (ii) Both supplier and customer should analyze the reason and discuss the disposition of non- conforming when the reason of nonconforming is not sure.

### 11-4. Agreement items

Both sides should discuss together when the following problems happen.

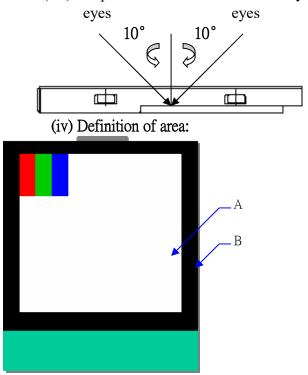
- a. There is any problem of standard of quality assurance, and both sides should think that must be modified.
- b. There is any argument item which does not record in the standard of quality assurance.
- c. Any other special problem.



- 11-5. Standard of The Product Appearance Test
  - a. Manner of appearance test:

(i) The test must be under  $20W \times 2$  or 40W fluorescent light, and the distance of view must be at  $30\pm5$ cm.

- (ii) When test the model of transmissive product must add the reflective plate.
- (iii)The test direction is base on around 10° of vertical line.
- (iiii)Temperature: 25±5°C Humidity: 60±10%RH



A. Area: Viewing area.

B. Area: Out of viewing area.

(Outside viewing area)

- b. Basic principle:
- (i) It will accord to the AQL when the standard can not be described.
- (ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
- (iii) Must add new item on time when it is necessary.
  - d. Standard of inspection: (Unit: mm)



#### 11-6. Inspection specification

Item		Specificatio	n			Unit : mm	AQL	
Electrical Testing	<ol> <li>1.4 Missing</li> <li>1.5 Missing</li> <li>1.6 Display</li> <li>1.7 No func</li> <li>1.8 Current</li> <li>1.9 LCD vie</li> </ol>	<ul> <li>2 Short</li> <li>3 T/P failure</li> <li>4 Missing vertical, horizontal segment, segment contrast defect.</li> <li>5 Missing character, dot or icon.</li> <li>6 Display malfunction.</li> <li>7 No function or no display.</li> <li>8 Current consumption exceeds product specifications.</li> <li>9 LCD viewing angle defect.</li> <li>10 Mixed product types.</li> <li>11 Flicker</li> </ul>						
	Product type	D						
		≪0.3	than five	ed (No more ve spots within 5mm)		X		
Black spots /	LAD	0.3≤D≤0.5	4					
White spots /Bright spots/		0.5 <d≤1.< td=""><td colspan="2">2</td><td></td><td>D=(x+y)/2</td><td></td></d≤1.<>	2			D=(x+y)/2		
Color spots		1.0 <d≤1.5< td=""><td colspan="2">2</td><td>1 Dro</td><td>duct's front side</td><td>2.5</td></d≤1.5<>	2		1 Dro	duct's front side	2.5	
/polluted inside/		D>1.5	NG		check	ed according to		
punctured	<ul><li>2.Printing ir</li><li>3、The part</li></ul>	ation, back side i nk peel off is not icle will be ignor paced: No more t	allowed. red when	it is removable	by clea			
	Product	W	L	Acceptable numb				
Linear Object: Fiber, scurf,	r LAD	≪0. 05	\$8	ignored No than five li within 5m	o more ines			
scratches and other linear defects (not		0.1<₩≤0.3	≪8	4 L		L	2.5	
affecting function)		₩> 0.3		NG			_	
	scratches fro	side scratches, n om the front side paced: No more t	is accepta	able		, cannot find the		



	s edge bing、edge kage			e can' t affect visual effection (edge ' t cause damage to circuit); over no visual damage conditions Acceptabl e numbers X≪3mm, Y≪2mm, Z≪T 5	) 2.	.5		
Glass	s broken	Vis	Visual broken is NG, and there is no potential fault.					
<ol> <li>V/A printed edges sawtooth inspected according to this standard</li> <li>LOGO's sawtooth</li> </ol>		typ	Product	<ul> <li>us defect judged according to samples</li> <li>Conditions</li> <li>1、 width below 0.2 inch (included)</li> <li>ignored, above 0.2 NG</li> <li>2、 Length not accounted</li> </ul>				
Specif	ic dimension		accordance w engineering s	ith product outline drawing or specification (key dimensi ample.	on) 2.	.5		
Glue overfl	ow/Frame	1. 0	Slue overflow	exceed 0.2mm to the black frame is not allowed.	2.	.5		
	Bonding bubble/ Misalignm ent	FPC golden finger hot pressure's bubble or impurity diameter shall be below 1/2 of the pressed area, pressed deviation shall not exceed 1/2 of the silver line width, and 40X microscope cannot have obvious cracks.						
FPC	Folded mark (minor fault)	Linearity irreversibility folded mark and acute angle folded mark is NG.						
	EMI FILM	Surface broken, scratched≦0.3mm Surface broken below 5mm can be modified by print ink, after modified, the result shall be achieved to EMI						



# **12. Handling Precaution:**

# 12.1 Warranty

This product has been manufactured to specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we will not take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1. We cannot accept responsibility for any defect arise after additional process of the product (including disassembly and reassembly), after product delivery.
- 2. We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 3. We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 4. We cannot accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product 3months from YEEBO production.
- 5. The liability of YB is limited to repair or replacement on the terms set forth below. YB will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between YB and the customer, YB will only replace or repair any of its CTP which is found defective electrically or visually when inspected in accordance with YB GENERAL CTP INSPECTION STANDARD.

# 12.2. Precautions in Use of CTP Module

### 12.2-1. Handling of CTP Module

12.2-1-1 Please operate the capacitive touch panel by touch the panel surface with finger or electric pen

12.2-1-2 Store the products at the temperature and humidity mentioned in the specification in a good package do not expose the products under direct sunlight.

12.2-1-3 Do not hit the capacitive touch panel in strong force , or drop it down, it is made of glass and friable.

12.2-1-4 Put on finger coats, glovers or mask to protect the products from fingerprint of stain. Do not upload/unload the touch panel by holding the FPC cable. Do not bend the FPC cableoften or pull it hard when installing, as FPC cable is soft and connected to touch panel body.

12.2-1-5 Pay attention to the prevention from high voltage and static electricity.

### 12.2-2 Storage

12.2-2-1 Store in ambient temperature of 25±5°C, and relative humidity of 50±10%RH. Do not expose to sunlight or fluorescent light.

12.2-2-2 Storage in a clean environment, free from dust, active gas, and solvent.

12.2-2-3 Store in anti-static electricity container.

12.2-2-4 Store without any physical load.

12.2-2-5 Appearance, 3months; Function, 1 year; within the validity, failed CTP can be replaced 1 to 1

### 12.3 Guarantee

Our products meet requirements of the environment.YEEBO ROHS requirement is based on European Union Directive 2011/65/EU (ROHS) Requirements and Update.

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